

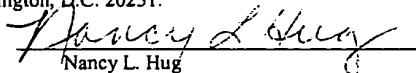


PATENT
Docket No. 248/225

CERTIFICATE OF MAILING BY FIRST CLASS MAIL

Date of Deposit: October 31, 2002

I hereby certify that this paper or fee is being deposited with the United States Postal Service as first class mail in an envelope on the date indicated above and is addressed to Commissioner for Patents, Washington, D.C. 20231.


Nancy L. Hug

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:)	Group Art Unit: 2812
)	
William H. Robertson et al.)	Examiner: Stacy A. Whitmore
)	
Serial No.: 09/514,674)	
)	
Filed: February 28, 2000)	
)	
For: AUTOMATED METHOD AND)	
SYSTEM FOR SELECTING AND)	
PROCURING ELECTRONIC)	
COMPONENTS USED IN CIRCUIT AND)	
CHIP DESIGN)	

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SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
Washington, D.C. 20231

Sir:

In accordance with 37 CFR §§ 1.97 and 1.98, the items identified in this Information Disclosure Statement (“IDS”) are brought to the attention of the Office. The items are listed on the attached form PTO/SB/08A (08-00). Copies of the items listed are enclosed herewith.

The items identified in this IDS may or may not be “material” pursuant to 37 CFR § 1.56. The submission thereof by Applicant is not to be construed as an admission that any such patent, publication or other information referred to therein is material or considered to be material (37 CFR § 1.97(h)), or even qualifies as “prior art” under 35 USC § 102 with respect to this invention unless specifically designated by Applicant as such.

INFORMATION DISCLOSURE STATEMENT FILING PROVISION:

- This IDS is believed to be timely in that it is being submitted under 37 CFR § 1.97(b), that is (1) within three months of the filing date of the application, which is not a continued prosecution application filed under § 1.53(d) or (2) within three months of entry of the national stage as set forth in 37 CFR § 1.491; or (3) before the mailing of a first Office action on the merits; or (4) before the mailing of a first Office action after filing a request for continued examination under § 1.114. Thus, no fee is required.
- However, if the undersigned is in error in this regard, Applicant respectfully requests that the Office consider this IDS as filed under 37 CFR § 1.97(c), if applicable, and charge the fee due under 37 CFR § 1.17(p) to the deposit account referenced below.
- However, if the undersigned is in error in this regard, Applicant respectfully requests that the Office consider this IDS as filed under 37 CFR § 1.97(c), if applicable, and a statement under 37 CFR § 1.97(e) is included below, thus no fee is required.
- This IDS is being submitted under 37 CFR § 1.97(c), that is after mailing of a first Office Action on the merits, but before a Final Action under 37 CFR § 1.113 or a Notice of Allowance under 37 CFR § 1.311.
 - The fee due under 37 CFR § 1.17(p) is submitted herewith.
 - A statement under 37 CFR § 1.97(e) is included below, thus no fee is required. In the event that this IDS is not received before a Final Action or a Notice of Allowance, then Applicant respectfully requests that the Office consider the filing of these papers to be submitted under 37 CFR § 1.97(d) and charge the fee due under 37 CFR § 1.17(p) to the deposit account below.
- This IDS is being submitted under 37 CFR § 1.97(d), that is after a Final Action under 37 CFR § 1.113 or a Notice of Allowance under 37 CFR § 1.311, but before payment of the issue fee. A statement under 37 CFR § 1.97(e) is included below. The fee due under 37 CFR § 1.17(p) is submitted herewith.
- This IDS is being submitted under 37 CFR § 1.97(i), that is after a Final Action under 37 CFR § 1.113 or a Notice of Allowance under 37 CFR § 1.311, but before payment of the issue fee.

STATEMENT UNDER 37 CFR § 1.97(e):

- Each item contained in this IDS was first cited in any communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this IDS.
- No item contained in this IDS was cited in a communication from a foreign patent office in a counterpart foreign application, and, to the knowledge of the person signing this statement after

making reasonable inquiry, no item of information contained in this IDS was known to any individual designated in 37 CFR § 1.56(c) more than three months prior to the filing of this IDS.

PAYMENT AND/OR AUTHORIZATION TO CHARGE FEES:

- A check in the amount of _____ is enclosed for the above fee(s).
- Please charge \$_____ to Deposit Account No. **50-1189** for the above fee(s).

The Commissioner is authorized to charge any fees required by the filing of these papers, and to credit any overpayment to Bingham McCutchen's Deposit Account No. **50-1189**.

Respectfully submitted,

Dated: October 31, 2002

By:



Erin C. Ming
Reg. No. 47,797

BINGHAM McCUTCHEN LLP
Three Embarcadero, Suite 1800
San Francisco, CA 94111-4067
(650) 849-4400

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Complete if Known

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INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(use as many sheets as necessary)

Sheet 1 of 2 Attorney Docket Number 248/225

U.S. PATENT DOCUMENTS

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FOREIGN PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Foreign Patent Document			Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ₆
		Office ³	Number ⁴	Kind Code ⁵ (if known)				
	5	EP	0433066		Hewlett Packard	06-1991		
	6	EP	0944002		Sony Europe	09-1999		
	7	EP	1063599		Dole	12-2000		
	8	PCT	WO 01/65422		Robertson et al.	09-2001		

Examiner Signature _____ Date Considered _____

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹ Unique citation designation number. ² See attached Kinds of U.S. Patent Documents. ³ Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ⁴ For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵ Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. ⁶ Applicant is to place a check mark here if English language Translation is attached.

Burden Hour Statement: This form is estimated to take 2.0 hours to complete. Time will vary depending upon the needs of the individual case. Any comments on the amount of time you are required to complete this form should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, Washington, DC 20231. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Assistant Commissioner for Patents, Washington, DC 20231

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INFORMATION DISCLOSURE
STATEMENT BY APPLICANT

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Sheet

2


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 PATENT & TRADEMARK OFFICE

of

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Complete if Known	
Application Number	09/514,674
Filing Date	February 28, 2000
First Named Inventor	William H. Robertson et al.
Group Art Unit	2182
Examiner Name	Stacy A. Whitmore
Attorney Docket Number	248/225

OTHER DOCUMENTS -- NON PATENT LITERATURE			
Examiner Initials *	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
	9	Benini, L. et al., "Distributed EDA tool integration: the PPP paradigm," Proceedings of the International Conference on Computer Design, 10/1996, IEEE, pgs. 448-453	
	10	Blanchard, D., "EDA on the WWW PCB CAD," Printed Circuit Design, Miller Freeman, 02/1998, vol. 15, no. 2, pgs. 34-37	
	11	Chan et al., "WELD - An Environment for Web-Based Electronic Design," Proceedings of the 35 th Design Automation Conference, 05/1998, pgs. 146-151	
	12	Geppert, L., "The Rising Tide of Web-Based Tools and Services Augurs the Next Sea Change in Electronic Design IC Design on the World Wide Web," IEEE Spectrum, 06/1998, vol. 35, no. 6, pgs. 45-50	
	13	Moretti, "Got IP," Printed Circuit Design, Miller Freeman, 10/1998, vol. 15, no. 10, pgs. 31-33	
	14	Perrin, B., "Web-Based Circuit Engineering," Circuit Cellar Ink, 03/1999, no. 104, pgs. 28-30, 33, 35, 37-38	
	15	Schindler, P. et al., "IP repository, a Web based IP reuse infrastructure," Custom Integrated Circuits, 05/1999, IEEE, pgs. 415-418	
	16	Konduri et al., "A Framework for Collaborative and Distributed Web-Based Design," Proceedings of the 36 th Design Automation Conference, 06/1999, pgs. 898-903	
	17	Wilsey, P., "Web-Based Analysis and Distributed IP," Proceedings of the 1999 Winter Simulation Conference, pgs. 1445-1453	
	18	Rao et al., "Web-Based Network Analysis and Design," ACM Transactions on Modeling and Computer Simulation, 01/2000, vol. 10, issue 1, pgs. 18-38	
	19	Wen et al., "Concurrent-Simulation-Based Remote IP Evaluation over the Internet for System-On-Chip Design," 14 th International Symposium on System Synthesis, 09/2001, pgs. 233-238	
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Considered

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